

Patent claims

1. A method for setting an operating parameter in a peripheral IC (12), in which method the operating parameter is transmitted from a central IC (15) via a bus connection (19) to the peripheral IC (12),
5 **characterized in that** the operating parameter is buffered in a preregister (24) of the peripheral IC (12) and that the buffered operating parameter is transferred to a working register (25) only if a transfer signal has
10 been sent from the central IC (12) via the bus connection (19).
2. The method as claimed in Claim 1, in which method the
15 bus connection (19) is a serial bus connection with a data line (Data), a control line (Start) and a clock line (CLK), and the transfer signal is transmitted via the control line (Start) to the peripheral IC (12).
- 20 3. The method as claimed in Claim 1 or 2, in which method the start of a data transmission from the central IC (15) to the peripheral IC is also signaled via the control line (Start).
- 25 4. A device for carrying out the method as claimed in one of the preceding claims with a central IC (15) and a peripheral IC (12), with a bus connection (19) between the central IC (15) and the peripheral IC (12), where the peripheral IC (12) has a working register (25) for
30 an operating parameter, **characterized in that** the peripheral IC (12) also has a preregister (24) for buffering an operating parameter which is received via the bus connection (19), and has means for transferring the buffered value to the working register (25), which
35 means respond to a transfer signal that is transmitted from the central IC (15) via the bus connection (19).
5. The device as claimed in Claim 4, in which device the bus connection (19) is a serial bus connection with a

data line (Data), a control line (Start) and a clock line (CLK), and the control line (Start) is used to transmit the transfer signal.

- 5 6. The device as claimed in Claim 5, in which device the control line (Start) is also used to transmit a start signal for data transmission from the central IC (15) to the peripheral IC (12).
- 10 7. The device as claimed in one of the preceding claims, in which device the peripheral IC (12) relates to a front-end IC for a communication arrangement for wireless data transmission and the central IC (15) relates to a signal processing device, with means for modulation or
15 demodulation of the mixed RF input signal and for further signal processing in baseband.
8. The device as claimed in Claim 7, in which device the operating parameter relates to the gain setting for the
20 receive gain in the front-end IC (12).
9. The device as claimed in one of the preceding claims, which device is configured as a send and receive device for wireless data transmission in accordance with the
25 HIPERLAN2 standard.